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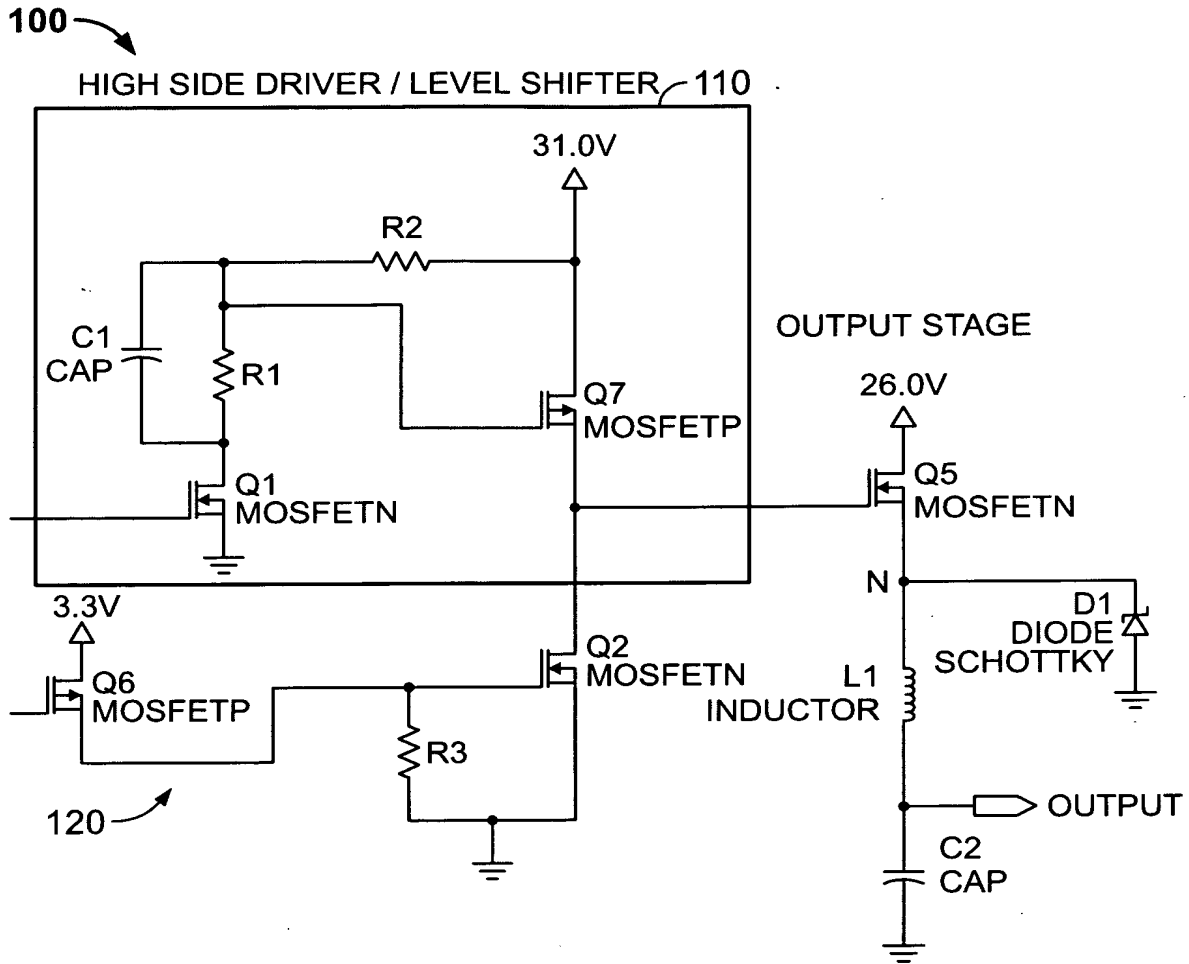


FIG. 1

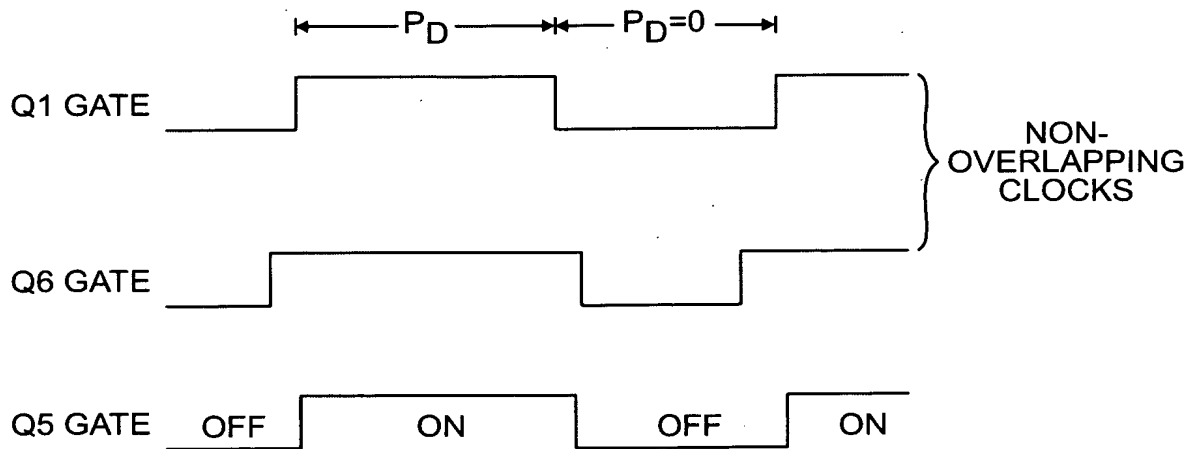
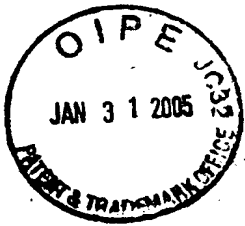


FIG. 2



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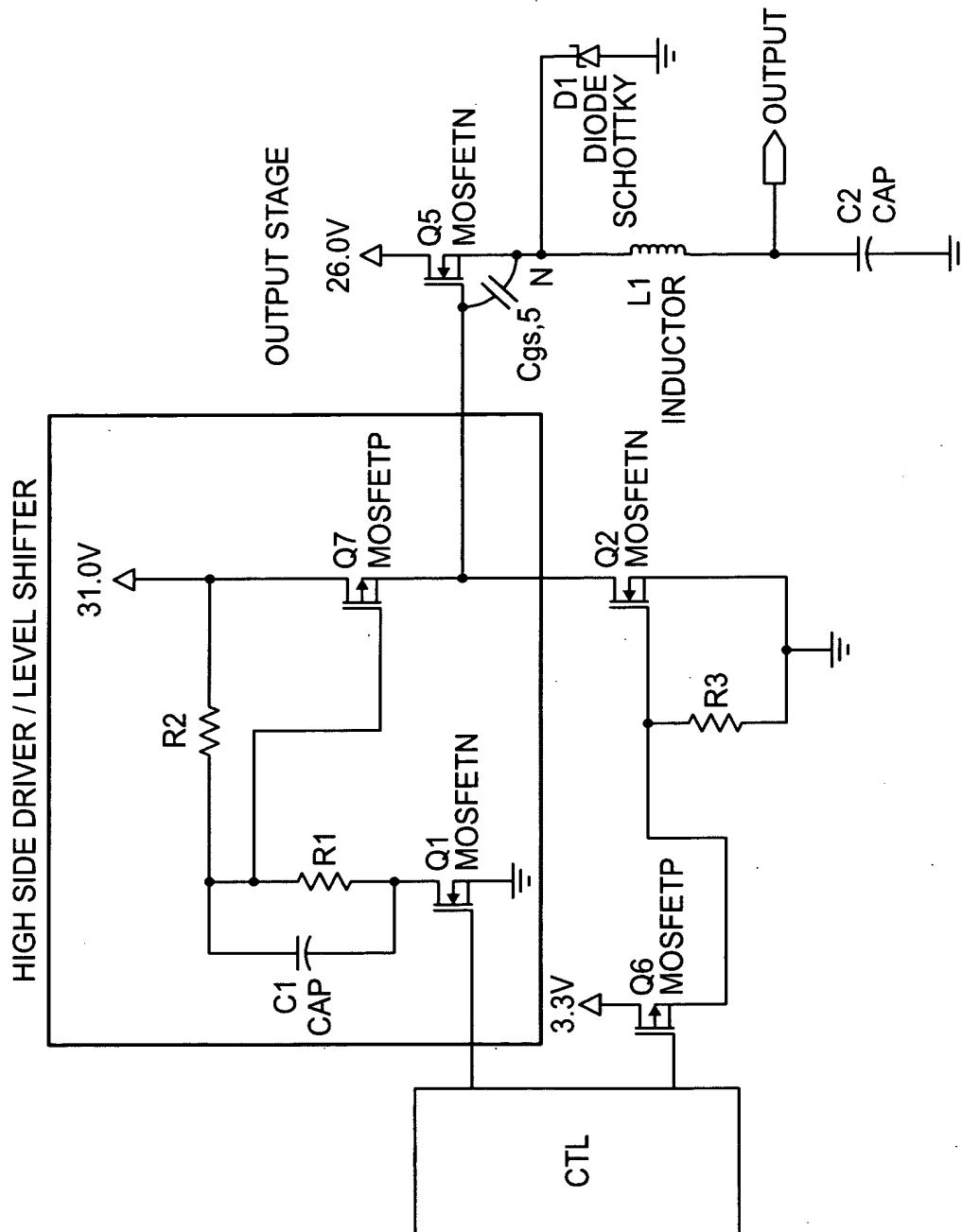
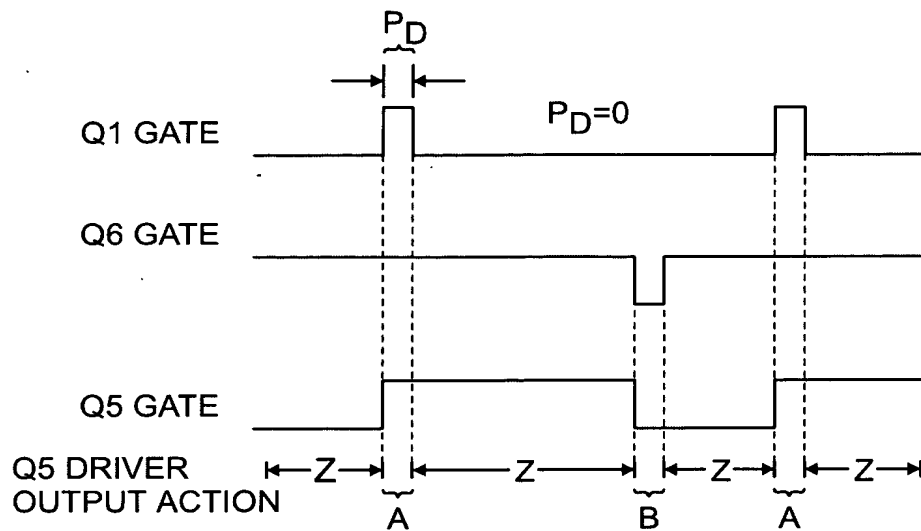


FIG. 3



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Z - HIGH OUTPUT IMPEDANCE
A - CURRENT SOURCED TO CHARGE Q5 GATE (CHARGE TRANSFER)
B - CURRENT SINK TO DISCHARGE Q5 GATE (CHARGE REMOVAL)

FIG. 4

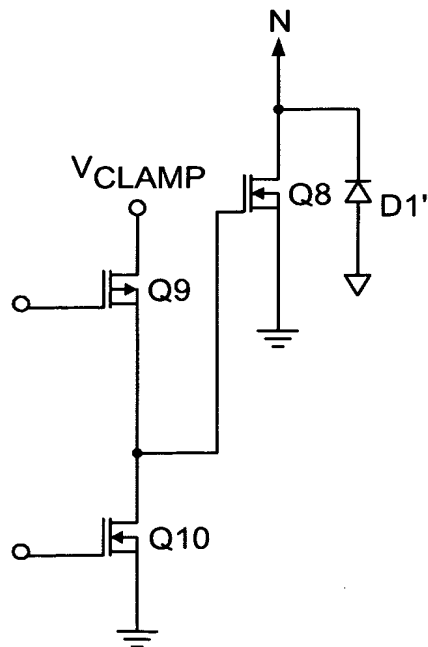
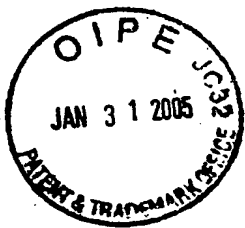


FIG. 5



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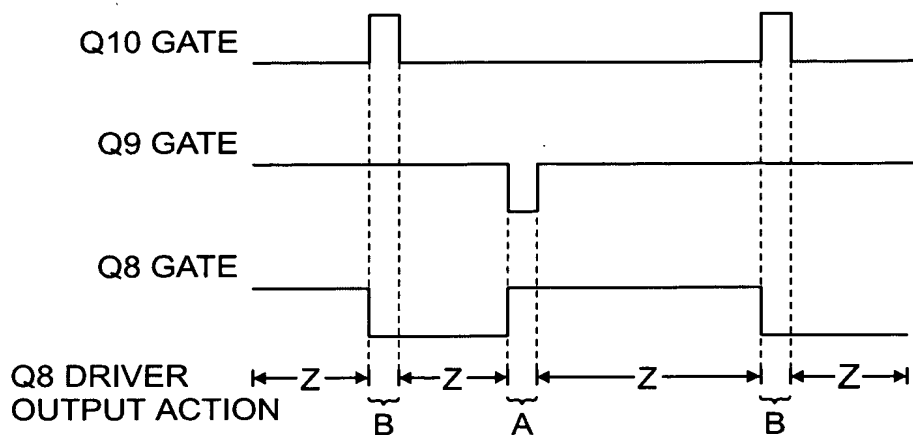


FIG. 6

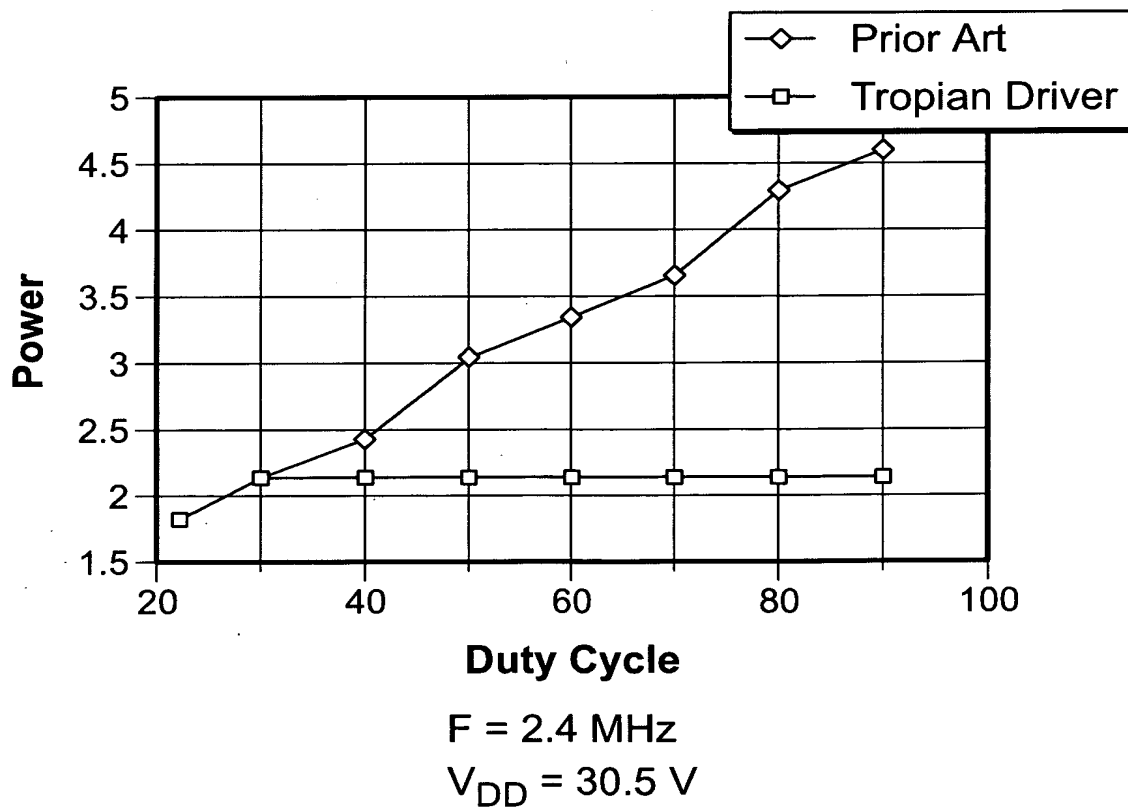


FIG. 7

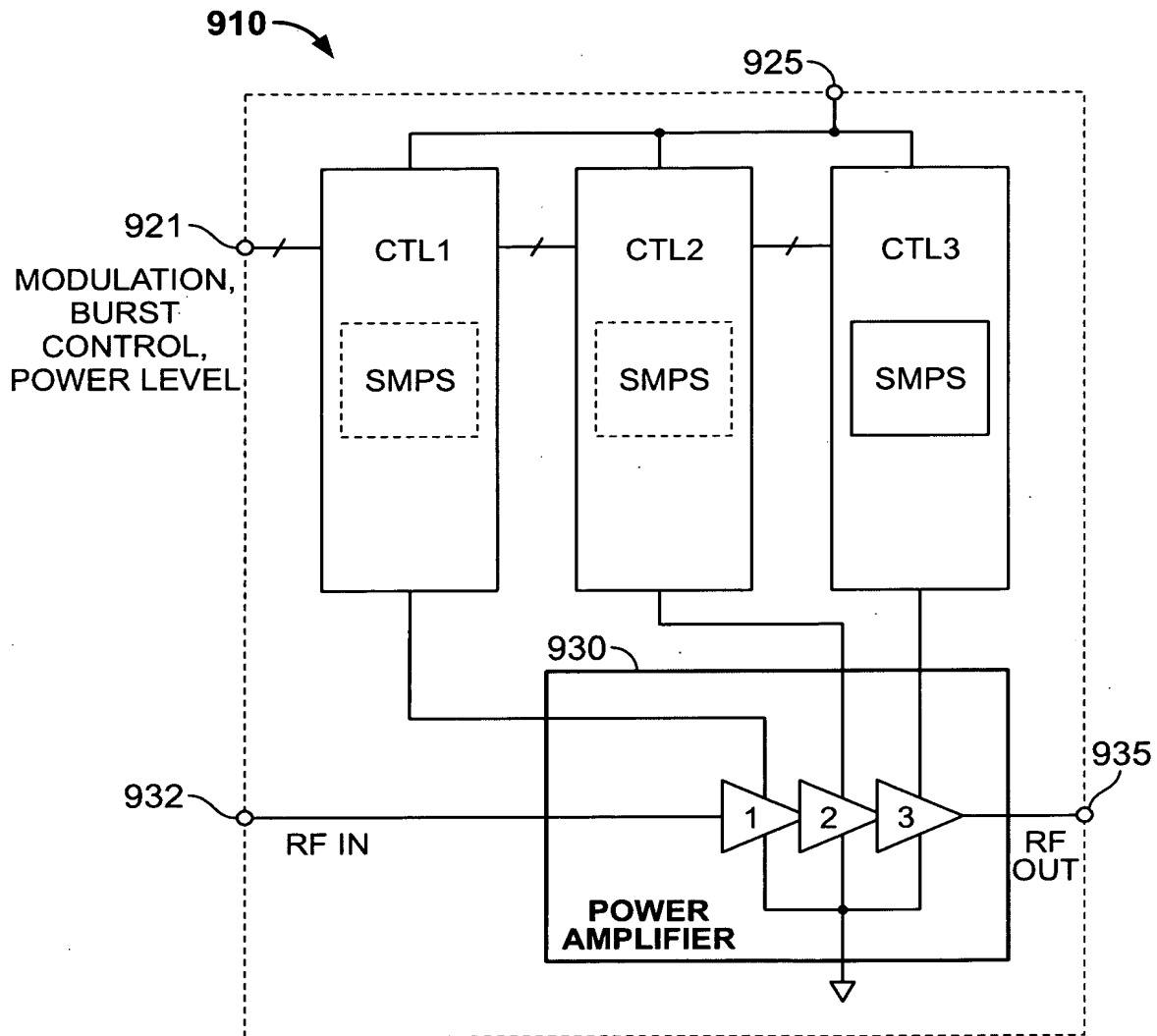
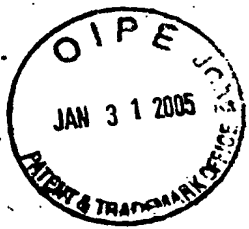


FIG. 8

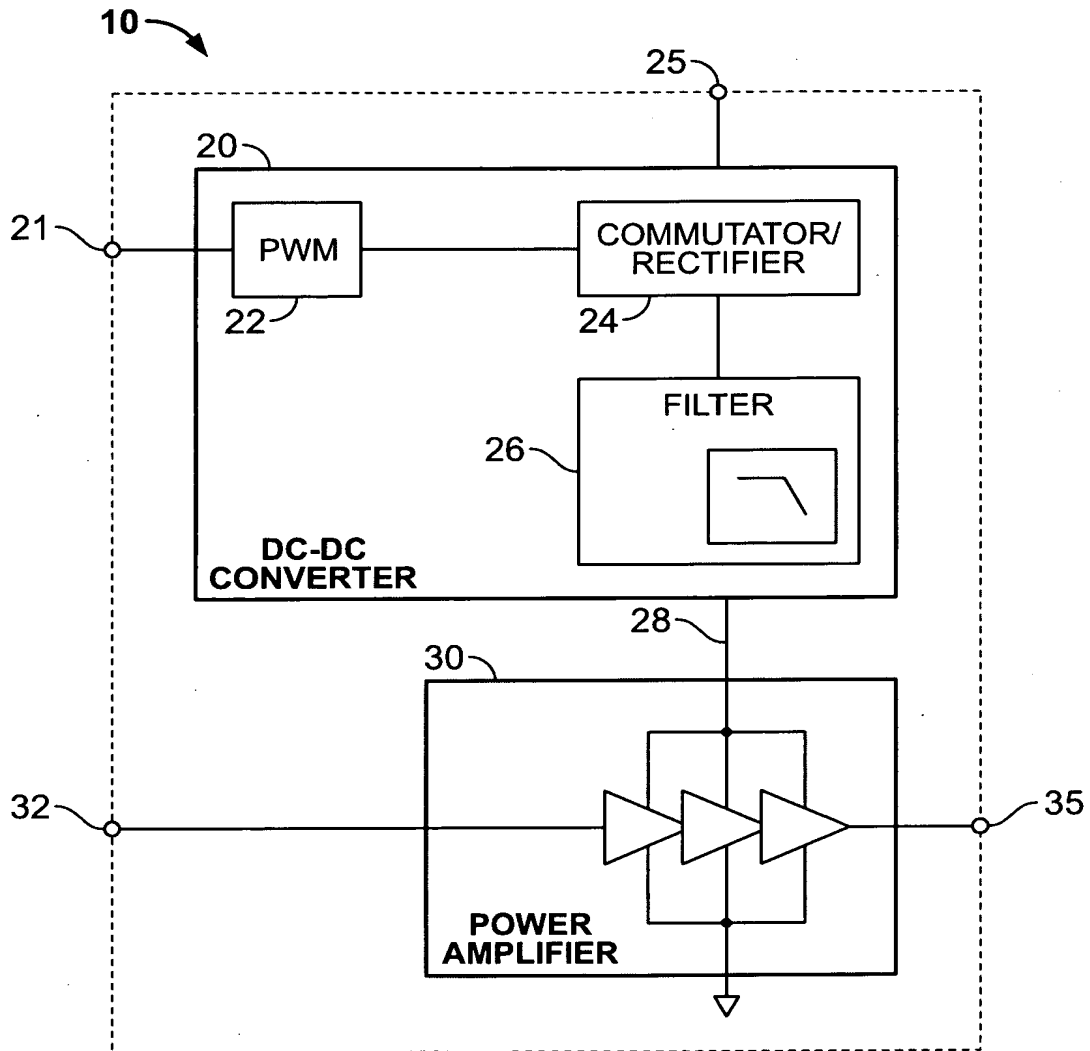


FIG. 9
(Prior Art)